



# **Absolute Maximum Ratings**

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Voltage at Any Pin -0.3V to  $V_{\mbox{DD}}$  +0.3V0°C to +70°C **Operating Temperature** Storage Temperature -65°C to +150°C

Package Dissipation 500 mW Maximum V<sub>CC</sub> Voltage Operating V<sub>CC</sub> Range 3V to 15V 300°C Lead Temperature (Soldering, 10 seconds)

16V

**Electrical Characteristics** 

 $T_A$  within operating temperature range,  $V_{SS}\,=\,GND,\,3V\,\leq\,V_{DD}\,\leq\,15V$  unless otherwise specified.

Parameter	Conditions	Min	Тур	Мах	Units
Quiescent Current Drain	$V_{DD} = 15V$			10	μA
Operating Current Drain	$V_{DD} = 10V$ , f <sub>IN</sub> = 4.19 MHz		1.2	2.5	mA
Frequency of Oscillation	$V_{DD} = 10V$ $V_{DD} = 6V$	DC DC		4.5 2	MHz MHz
Output Current Levels	$V_{DD} = 10V$ $V_{O} = 5V$				
Logical "1" Source		500			μA
Logical "0" Sink		500			μA
Output Voltage Levels	$V_{DD} = 10V$ $I_O = 10 \mu A$				
Logical "1"		9.0			V
Logical "0"				1.0	V

Note: For 3.58 MHz operation,  $V_{DD}$  must be  $\geq$  10V.

# **Functional Description**

A connection diagram for the MM5369 is shown in Figure 1 and a block diagram is shown in Figure 2.

#### TIME BASE

A precision time base is provided by the interconnection of a 3,579,545 Hz quartz crystal and the RC network shown in Figure 3 together with the CMOS inverter/amplifier provided between the OSC IN and the OSC OUT terminals. Resistor R1 is necessary to bias the inverter for class A amplifier operation. Capacitors C1 and C2 in series provide the parallel load capacitance required for precise tuning of the quartz crystal.

The network shown provides > 100 ppm tuning range when used with standard crystals trimmed for  $C_L = 12 \text{ pF}$ . Tuning to better than  $\pm 2 \text{ ppm}$  is easily obtainable.

## DIVIDER

A pulse is genertaed when divider stages 1 through 4, 16 and 17 are in the correct state. By mask options, this pulse is used to set or reset individual stages of the counter. Figure 4 shows the relationship between the duty cycle and the programmed modulus.

### OUTPUTS

The Tuner Output is a buffered output at the crystal oscillator frequency. This output is provided so that the crystal frequency can be obtained without disturbing the crystal oscillator. The Divide Output is the input frequency divided by the mask programmed number. Both outputs are push-pull outputs.



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